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DESCRIPTION AND OPERATION OF THE GENERAL PURPOSE VARIABLE DELAY UNIT

REPORT 891

RADIATION LABORATORY
MASSACRIBUTE INSTITUTE OF TECHNOLOGY
CAMBRIDGE
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Radiation Laboratory

Report 891

March 26, 1946

DESCRIPTION AND OPERATION OF THE CENERAL PURPOSE VARIABLE DELAY UNIT

Abstract

The general purpose variable delay unit is a device designed for obtaining a trigger pulse delayed in time from an imput trigger. The amount of delay time can be varied continuously from a fixed minimum value of about 10 microseconds (approximately 1 statute mile) to a maximum of about 2400 microseconds (approximately 200 nautical miles), depe ding of course on the period between input triggers. In fact, the maximum delay can be up to 85 percent of the duration of this period. The unit will accept a positive or negative trigger rising to at least 50 volts in .5 microsecond and supplies a positive or negative trigger output. The output triggers rise to 90 percent of maximum amplitude in .2 microsecond with positive output of 70 volts at 75 ohms impedance and negative output of 200 volts at 4000 ohms impedance. The delay circuit used is the phantastron using the type 65A7 tube. The unit operates from a line voltage of 115 volts at 50 to 1200 cycles per second and when constructed as illustrated will weigh about 20 pounds.

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Approved by.

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Title page 10 numbered pages 7 pages of figures

V-22525

DESCRIPTION AND OPERATION OF THE GENERAL PURPOSE VARIABLE DELAY UNIT

1. Introduction.

The delay control in the general purpose delay unit is derived from an electronically regulated d.c. variable control voltage in a phantastron circuit using a 6SA7 tube. The phantastron circuit is used because it provides for a very linear variation of delay time, and hence, allows accurate and reliable calibration of a dial associated with the control voltage variable.

A detailed discussion of the phantastron principle can be found in Radiation Laboratory report, No. 63-21.

In addition to the phantastron circuit, other circuits are required to provide proper input voltages and for the derivation of the output trigger from the output voltage of the phantastron circuit. The block diagram in Fig. 1 indicates the sequence of circuit operation.

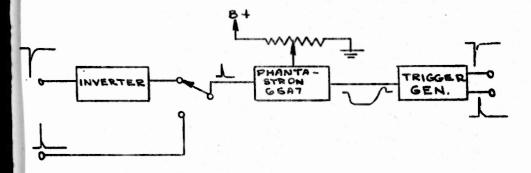


Fig. 1

In this unit a 30 volt positive trigger is applied to grid No. 2 of the 6SA7. This grid acts as a second control element in this tube. The resulting action results in a negative vaveform at the cathode whose duration is proportional to the value of the control voltage derived from a potentimeter across the supply voltage. The positive variation of cathode voltage corresponding to the recovery action of the circuit is linear with time. Furthermore, the slope of this variation is constant with respect to control voltage and hence the duration of the cycle. At some point along this slope the bias on a trigger generator stage is overcome, resulting in one cycle of operation of the trigger generating circuit.

The trigger generator is a blocking oscillator normally biased to cutoff and its operation is thus delayed from the input trigger by an amount of time determined b/ the control voltage in the phantastron circuit.

A fast rising pulse occurring in the cathode circuit of the blocking oscillator forms the positive trigger output. A negative trigger is obtained by inverting this positive trigger in the second half of Vl.

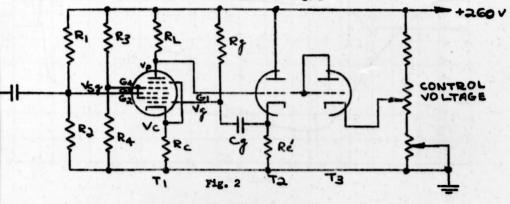
2. Circuits.

2.1 <u>Inverters</u>. As mentioned in section 1 two inverters are used in this unit and utilize the two sections of a 65N7 tube. One provides the positive trigger for the phantastron from the negative trigger input terminal while the other inverts the positive output trigger to make a negative trigger available from the delay unit.

In the first case the triode section is normally conducting at zero bias. The negative input momentarily cuts off plate current allowing a fast rising positive signal to develop in the plate circuit. The plate load consists of R5 in parallel with R6 and R10 through C5. C4 in the grid circuit of VIA prevents parasitio oscillation that was previously observed at this point.

In the second application of the inverter the second section of VI is biased beyond plate current cutoff. The grid is direct coupled to the cathode circuit of the trigger generator tube. This point of connection is a fraction of a volt above ground potential except when a current pulse flows through it due to the operation of the trigger generator.

2.2 Phantastron delay circuit. The phantastron circuit consists of a 68A7 (V2) in conjunction with both sections of a 68N7 (V3). Following, for the most part, Radiation Laboratory report 63-21, we divide the operation of the circuit into six stages. The basic circuit is shown in Fig. 2 and the waveforms at the various electrodes are shown in Fig. 3



891-2

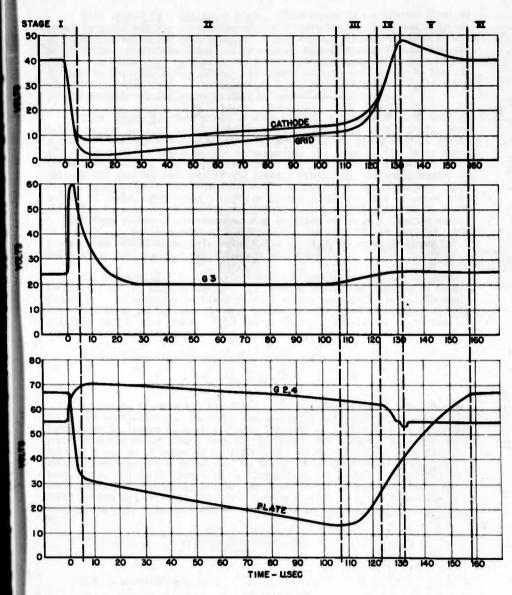


FIGURE 3

2.21 Stage VI - Quiescent Stage. This stage is considered first in order to point out the conditions of the circuit before and after a cycle of operation initiated by a trigger. Plate current is cut off by a G₂ bias of 15 volts. The plate potential is held at approximately the control voltage by the diods T₂. G₁ bias is held near zero by means of G₁ current through R₂ tied to B+. The screen, G₂,4, is drawing about 4 ma. mostly to the collector plate tied to G₂. Consequently, V₀ is about 40 volts. C₂ is charged to the control voltage minus initial G₁ potential.

2.22 Stags I. This stage of operation is initiated by a 30 volt positive trigger applied to G_3 , the second control element in the 6SA7. Space current is quickly transferred from the screen to the plate circuit cutting off the diods in a fraction of a microsecond. V_D drops as the stray capacitance from plate to ground ie discharged. This drop in V_D is passed to G_1 through the cathode follower T2 and closely behind G_1 . In other words, the signal on G_2 removee its bias momentarily transferring space current from screen to plate. But plate current is such a small fraction of space current that V_C also drops and the regenerative action from plate to G_1 keeps G_1 bias from changing appreciably. The duration of stage I depends mostly on R_L C_{Stray} but somewhat on the shape and duration of the trigger. Stage I ends when squilibrium plate current flows. R_LC_G is large compared to R_LC_{Stray} so that C_G does not discharge appreciably.

2.23 Stage II. An unstable condition now exists because G_1 current is cut off and G_2 must discharge. It does so through R_2 causing V_2 to rise toward B+. An increase in plate current occurs so that V_2 falls further. This fall in V_2 is fed back to G_1 . Screen current also increases but only slightly during this stage. This action then corresponds to the action in a Miller fsedback circuit.

Vp decreases exponentially with time, i.e.,

$$V_p = K_1 + K_2 e^{-\frac{C}{T}}$$

 K_1 and K_2 involve all the circuit and tube parameters. (See report 63-21 for derivations.) In order for the delay characteristic of the phantastron to be linear V_D must decrease linearly so that T must be large compared with any delay time t that may be used. For a high μ tube such as the 68A7 in a region of moderate G_m (1000), $T\simeq \mu C_s R_g$. Stage II ende when V_D and V_{SG} reach so low a value that i_D ceases to increase with decreasing bias on G_1 . By definition this is a region of low G_m and thus V_D falls nonlinearly and eventually becomes constant (about 27 velts in the delay unit) at the snd of stage II. This constant value of V_D is independent of control voltage. The duration of this stage is given approximately below by

$$\frac{\text{C.V.} - \Delta V_{I} - 27}{\text{E}_{b}/\text{R}_{g}\text{C}_{g}}$$

C.V. = control voltage

 $\Delta V_{\underline{I}} = \text{drop in } V_{\underline{D}} \text{ during stage I.}$

2.24 Stage III. During this stage v_0 is at its lowest possible value so that no feedback can occur between plate and grid. v_g rises toward B+ with the time constant $R_g Q_g$ with v_0 following along closely and the screen taking most of the increasing space current, thereby decreasing v_{gg} . ip should increase, but C5 bias is decreasing as U_0 increases so that ip decreases slightly, causing v_0 to rise. v_0 increases finally to the point where R_g bias approaches plate current out off, warking the end of stage III.

2.25 Stare IV. The regenerative notion again takes hold at the beginning of this stage and veries with Vp. Vag falls sharply now since ip is being out off and the screen is taking up the space current as 65 bias increases. Vp rises at a rate determined by R. Z C stray, as in stage I, and the shurpmess of 65 ip out off bias. This stage ends then screen current becomes constant regardless of 61 bias. C; current begins to flow a sin eventually clamping 61 to the quie cent bias condition at the end of strage V. Vg at the beginning of and its rate of rice during this stage in independent of control voltage, and at some point of this rise, the bias of the following amplifier stage, V4 is overcome so that the output trigger is formed.

2.26 Stars V. Vp continues to rise according to R_L X 0 stray until it is caught by the diodo and elemped to the control voltage. Ve has reached its quiescent voltage sometime during stage IV, but overshoots it in following G₁ beyond its quiescent point due to feedbeck from the plate through the cathode follower, T2. The shape of this overshoot is due to the current pulse in G₁ and its durition turns out to be proportional to the duration of the entire cyclo, and hence, proportional to the control voltage. This fact is unimportant since the exximum possible duty cycle remains about 85 percent for any input trigger repetition fracturing.

2.3 Amplifier and Trigger Goverator. The critical trigger is formed in V4 and V5 which are both type 6AG7 tubes. Since interelectrode capacity is low in this type it is possible to obtain very rapid action especially when large amounts of feedback are introduced as with a secondar/ winding of a pulse transformer. In the circuit used in the delay unit, V4 acts as a current amplifier which is normally cut off. When the cathode of V2 rises toward its stable value at the end of stage V, V4 grid is carried into the plate current conducting region and a fast current pulse is pulled through the plate winding of T1. Coupling to the grid winding is such that V5 grid is driven positive momentarily causing plate current to flow in V5, further amplifying the current pulse in V4. Saturation current is quickly reached and when dip/dt becomes zero, grid current in V5 and R28 discharges C12, and the feedback action again takes hold so as to drive V4 grid strongly negative, cutting off V5 plate current. Now C12 can discharge only through R28, sesulting in a comparatively slow recovery of V5 grid. In the meantime, during the positive portion of the grid signal, a current pulse will be formed ecross R27. This current pulse reaches a peak about .5 ampere causing a 75 volt trigger to appear at this point at a very low impedance level, which is of the order of 50 ohms, i.e.,

$$Z_o = \frac{R_{27}(r_p + Z_t)}{(\mu+1) R_{27} + x_p + Z_t}$$

This positive trigger is available at the panel of the instrument and is also direct coupled to the grid of the inverter ne described in 2.1 to obtain a negative trigger for external use. Since no oscillation in T_1 can take place while the grid of V_5 is held negative by C_{12} , plate current in V_4 deteriorates to a stable value ($\underline{\circ}$ 0) and will not change again until the end of the next cycle of operation of the phantastron.

3. General.

It is essential to use high quality components in this unit if reason able reliability of calibration of delay time is to be expected. This is especially true for the phantastron circuit itself. For minimum variation due to temperature changes the resistors specified for R7 to R19 inclusive should be of the types recommended in the parts list at the end of this manual. C5 and C6 should be of high grade mica such as JAN CM30E and JAN CM35E, respectively.

When tube replacements become necessary for V_2 and V_3 , $R_1 \lambda$ and R_{19} can be readjusted to reset the delay to the dial calibration.

Standard receiving type tubes were used since they are more generally available. Miniature equivalents of these tubes could be used with some circuit modifications if epace considerations demand it. In fact, some advantage may be gained in respect to jitter due to line voltage ripple if a 6AS6 (miniature) is used instead of the 6SA7. It was found that heater map ply ripple in the 6SA7 caused a constant jitter of about 0.1 µsec. in the output trigger. This jitter would disappear if d.c. were used for heater supply. It also seems to depend on the time constant in the grid circuit since it was .02 µsec. when C5 is used and 0.1 when C6 is used for the de lay range.

Fig. 4 is a graph of the amount of delay as a function of the dial set ting. It is thus seen that the delay is directly proportional to the dial reading. However, it will be noted that this delay is not linear for the first 3 to 12 divisions on both the low and high delay range corresponding to C5 and C6 respectively on switch Sw. 2.

To assist in locating trouble when maintenance is necessary, the table at the end of this text may be referred to for socket terminal voltages that should normally be read with a 20,000 ohm per volt multimeter. An oscilloscope would be desirable for checking wave forms that should appear at these points. Review of the preceeding text and reference to the schematic diagram should indicate what these wave forms should be.

As shown in the photograms of Fig. 5, 6, 7, and 3 the packaging util izes an SAR type chassis and dust cover. In addition, if the power transformer* ejecified in the parts list is used, this unit will be suitable

^{*} At the time the model shown in the photographs was built this transformer was not yet available. About 4 pounds will be added to the weight of the unit if it is used.

for airborne use and should give trouble free service under most conditions normal to airborne applications. Under normal room conditions, the operating temperature inside the unit reaches approximately 58°C.

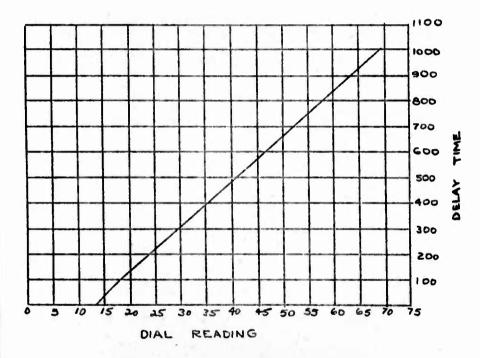


Fig. 4

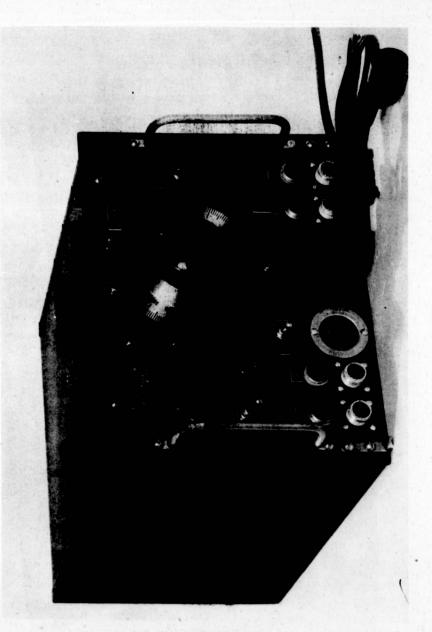


FIG. 5 VARIABLE TRIGGER DELAY UNIT

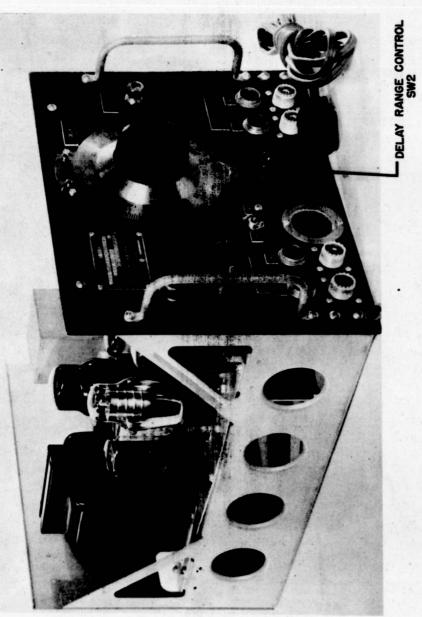
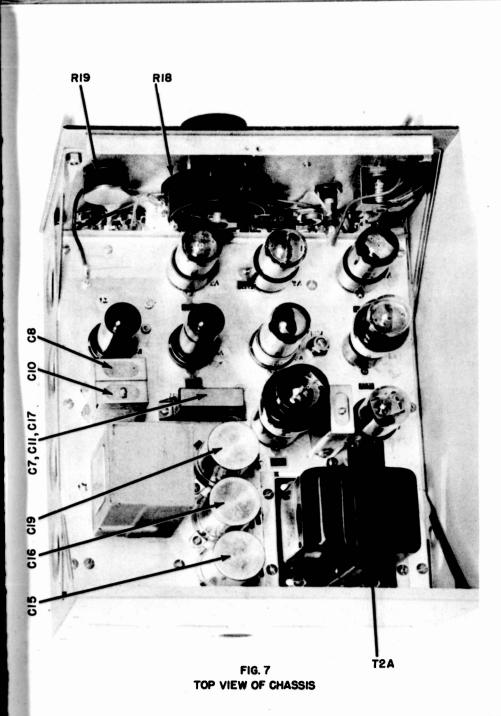
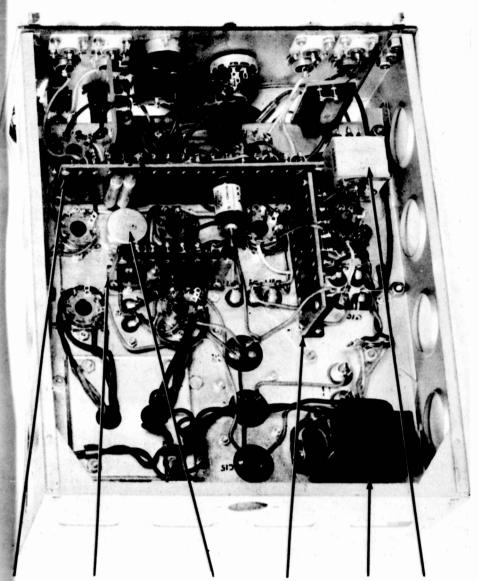


FIG. 6
OBLIQUE VIEW OF UNIT





TERMINAL TERMINAL STRIP #3

RI4

TERMINAL STRIP #2 T2B

TI

FIG. 8
BOTTOM VIEW OF CHASSIS

TUBE SOCKET VOLTAGES

Rep. rate = 500 ~ Delay ≅ 200µsec

	1	2	3	4	5	6	7	8
V).	6	100	0	.1	260	33	3ac	350
<u> </u>	0	3ac	85		37	38	3ac	21.5
<u>v</u> 3	85	260	95	85	85	95	3ac	Зас
V4	49	3ac	0	38	49	205	3ac	255
V5	20	3ac	0	6.8	20	250	3ac	250
<u>v6</u>	0	360	0	320ac	0	320ac	0	360
V 7	260	260*	350	340	250	0	260*	260
V8	0	3ac	205	102	105	135	3ac	250
V 9	0	0	0	0	105	0	0	0

^{* 6.3}V a.c. between these terminals

PARTS LIST - GENERAL PURPOSE VARIABLE DELAY

			Y.		
			Voltage		
Component Value		Rating	Tolerance	JAN if any	
R4		470K	1/2	10 percent	RCPOBF474K
R5		39K	28	10 percent	RC41AL393K
R6		39K	2N	10 percent	RCALAL393A
R7		91K	18	5 percent	RC30AE913J
R8	W	15K	100	5 percent	Koolohm Type KT
R9		îÑ	10	5 percent	RC31AE105J
R10	Wil	10K	10%	5 percent	Kooloha Type KT
R11		6.8K	27	5 percent	RC41AE682J
R13		10K	1W	5 percent	RC30AE103J
RLA	pot.	100K	1/2W	20 percent	
R15	•	1M	1/2W	1 percent	Precision wire wound
R16		47K	177	5 percent	RC30AE473J
R17	WW	12.54	1CW	5 percent	Koolohm Type KT
R18	pot.	20K	AW	1 percent	DeJur Amsco No. 281
R19	pot.	5K	2W	10 percent	RA2CA150502AK
R20	-	24K	19	5 percent	RC30AE243J
R21		82K	177	5 percent	RC30AE823J
R22		2K	1/2W	5 percent	RC20BF202J
R23		18K	1W	5 percent	RC3OAE183J
R24		5.1K	177	1.0 percent	RC3OAE512K
R25		220K	19	5 percent	RC30AE224J
R26		15K	1%	5 percent	RC3OAEL53J
R27		150-	1W	5 percent	RC3OAE151J
R28		68K	1 2W	5 percent	RC20BF683J
R29		10K	1/29	10 percent	RC20BF103K
R30		33K	1/2W	10 percent	PC208F333K
R31		22 0K	1/2W	10 percent	RC20BF22AK
R32		470-2	1/24	10 percent	PC20BFA7AK
R33		114	1/20	10 percent	RC20BF105K
R34		12K	29	10 percent	RC41AE123K
R35		2.7K	1W	10 percent	RC30AE272K
R36		470K	1/21	10 percent	RC20BF474K
R37		150K	18	5 percent	RC3OAE15AJ
R35		10 0 K	17	5 percent	RC30AE104J
Cl		. 002/HZ	600V	20 percent	metal cused oil im-
C2		200			pregnated paper
C3		200mg		5 percent	CM20A201
67		43mg		5 percent	C#50V501
C5		.0012μf		10 percent	CH20A430
66		.0062ht		5 percent	CM30E122
C7		0.luf		5 percent	CH35E622
C8		1.011			triple with Cl18017
C9				10 77	Tobe OM601
37		100µп		10 percent	CM20A101

		Power or			
		Voltage			
Component	<u>Ynlue</u>	Rating	Tolerance	JAN if any	
C10	1.Out			Tobe OM601	
Cll	0.1µf		Triple with C'	7 and C17	
C12	.0018uf			CM30E182	
C13	.01µf		Sprague metal cased oil paper		
C14	.Oluf				
C15	Sur	450	Sprague D11167	7	
C16	8µ£	450			
C17	ينبلا.0	600		Aeravox 618CB	
C18	0.1μΓ	60U		Tobe 06610	
C19	8µf	450	Sprague D1116	7	
L1	10H filter	choke	50ma 240	Freed 12185	
Tl T2	Pulse trans		Utah X154T mg. A14730A to F in	nol. C6769A.	
	C11952A, C1	2582D; or 170R	78 and T19F97	,,	
Sw 1	SPDT Toggle	ST12D			
Sw 2	Rotary SPDT				
Sw 3	SPST Toggle	ST12A			
	9 Otal tube sockets molded mice base bakelite for 1" dia. chassis hole Dial - National type N4 Fuse holder for 3AG fuses Fuse 3AG 2 amp. Pilot Bulb and holder Miniature Dayonette Chassis SARCM type B-13 Dust cover SARCM type B-13				
P1 P2 P3	Motor plug, m Motor plug, fo Line cord plug Line cord 5 fo	emale, cable, t	wist lock, Hubbel :	ziniature ziniature	
J1,J3,J5,J J2,J4,J6,J		SCS chassis on Jack - AN No. S	ble connector 0-239, Navy No. 49	194	
V1 V2 V2 V4 V5 V6 V7 V8 V9	6SN7 tube 6SA7 tube 52N6GTT tube 6AG7 tube 6AG7 tube 5Y3G tube 6Y6G tube 6SJ7G tube VR1.05 tube				

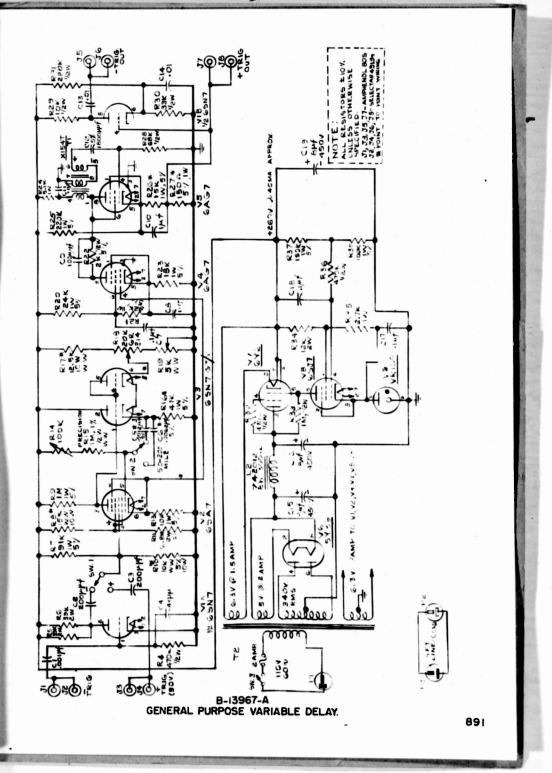
Hardware .

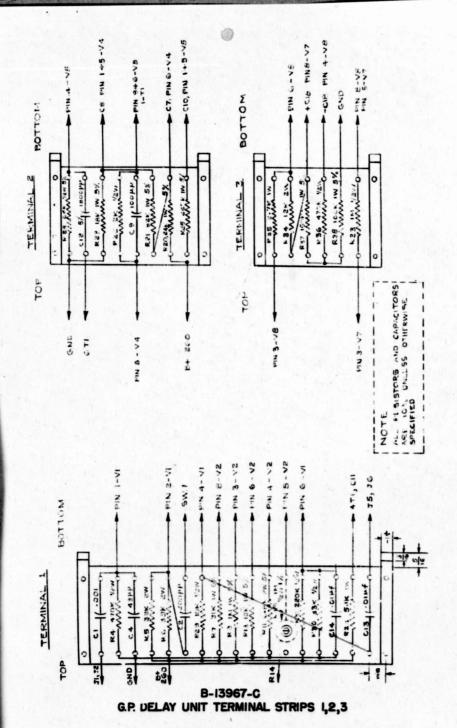
All screws end nuts required must be cadmium plated. Use either elastic stop nuts or plain nut and lock washer and slotted head screws.

Mount potentiometer, R13, on 1/2 inch spacers, brass.

Terminal strips R.L. dwg. B13967C Parel drilling layout R.L. dwg. B13967D Chassis drilling layout R.L. dwg. B13967F

> R. P. Abbenhouse October 26, 1945





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U.S. Eng.	Unclass. Mar'46 18 10 photos, diagrs, graphs			
	ADSTRACT			
from an input to value of about period between rising to at lead output. It open	delay is a device designed for obtaining a trigger pulse delayed in time tigger. The delay time can be varied continuously from a fixad minimum 0 microseconds to a maximum of about 2000 microseconds depending on the nput triggers. The unit will accept a positive or negative trigger st 50 volts in 0.5 microsecond and supplies a positive or negative trigger attes from a line voltage of 115 volts at 50 to 1200 cycles per second and ximately 20 pounds.			

T-2, HQ., AIR MATERIEL COMMAND AIR VECHNICAL UNDEX WRIGHT FIELD, OHIO, USAAF

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